Using Sedona 1.2 Components



Using Sedona 1.2 Components from Tridium's Kits

Introduction

This application note assists in the understanding of the Sedona components provided in Tridium's Sedona-1.2.28 release. Some of the Sedona components were changed or added since the previous release. New with the 1.2 release is that the Sedona components, previously concentrated in one Control kit, are now organized in smaller kits under a functional name. Components discussed in this document can be found in the following kits:

- basicSchedule
- datetimeSTD
- func
- hvac
- logic
- math
- pricomp
- timing
- types

The intent of this document is to explain the potential use of those components supplied by Tridium in their Sedona 1.2 release. All are included in Contemporary Controls' BASremote and BAScontrol product families. They have not been modified for use in these products. Contemporary Controls has product specific Sedona kits that address the uniqueness of the IO structure in the BASremote and BAScontrol products. These kits are not mentioned in this document. It is Contemporary Controls' policy to provide all Sedona kits to the Sedona Framework community without charge or license. This includes kits obtained from Tridium, kits with modified Tridium components, kits developed solely by Contemporary Controls to improve the control options available to systems integrators, and kits specific to Contemporary Controls' hardware. Any feedback is welcomed.



Variable Types

Although there are several variable types used by Sedona, three are the most interesting — Boolean, Float and Integer. You can define constants for each type and use converting components to change the data representation to a different type.



Configuring Constants



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Using Write Components



Converting Between Component Types



Float-to-Boolean and Boolean-to-Float Conversion





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Negating a Boolean Variable — Inverting Your Logic



A		NotA logic::Not		NotNotA logic::Not		
types::ConstBool	-	Out	false	Out	true	Variable A is now set to be
Out	true	In	true	In	false	true. Notice the output of the
					first inverter changes the	
B		NotB logic::Not		NotNotB logic::Not		value of A to a false while the
types::ConstBool	-	Out	true	Out	false	second inverter restores the
Out	false	In	false	In	true	state of A back to true.



Boolean Product — "ANDing" Boolean Variables





A types::ConstE	Bool O		
Out	false	And2 logic::And2	8
		Out	false
		In1	false
	(In2	true
B types::Const	Bool		
Out	true		
F	or an AND gate	e, if A is false and	В
	true then the	output is false	



Boolean Sum — "Oring" Boolean Variables









Exclusive OR — A OR B but Not Both



Cascading Logic Blocks and Unused Inputs



Cascading Logic Blocks and Unused Inputs (continued)







Boolean, Float or Integer Selection





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Boolean, Float or Integer Selection (continued)

start Source	ASW4 [Target]			
O Mata	Mata			
O Out	Out			
O Set				
O out	○ In2			
	In3			
	🔘 In4			
	Starts At			
	Sel			
	(\$)			
ink Istart.out -> ASW4.sta	rtsAt			
	OK Cancel			
Out 0				
L	This is the dialog screen you will see when you need to			



De-Multiplexing





Float Addition



Float Subtraction



Float Multiplication



Float Division

Afloat types::ConstFloat		Div2		Division is also straight forward.
Out	8.00	Math::Div2 Out In1	2.00	Input 1 (In1) is the dividend, input 2 (In2) is the divisor and the
Bfloat types::ConstFloat	tFloat	4.00 false	output (Out) is the quotient. Dividing by zero will result in the	
Out	4.00			pin Div0 being set to true.

Finding Minimums and Maximums

Afloat 💿	Max math::Max		
types::ConstFloat	Out	8.00	The Max component output (Out)
Out 8.00	In1	8.00	me max component output (out)
	In2	4.00	reflects the maximum value of the two input floats (In1, In2) while the
Bfloat types::ConstFloat	Min math::Min		Min component reflects the
Out 4.00	Out	4.00	minimum value of the two inputs.
	In1	8.00	
	In2	4.00	

IRamp 😡	12F	MinMax math::MinMax		The MinMax component is a bit more complex. There is only one
func::IRamp Out 8	types::I2F In 8 Out 8.00	8 Max Out 8	.00	input and two outputs. If R is held in the true state, the two outputs
		.00 In 8	.00	
		R f	alse	simply reflect the input state. If R
			is false, the Min Out captures the	
To demonstrate	e this operation,		lowest value of the input while Max Out captures the maximum o	
configured to g	enerate a triang			
minimum value	e of 4 and a maxi	mum of 8. The		the input. When connecting up th
MinMax compo	onent captured t	he limits. Notice		component for the first time you
the need of an	Integer-to-Float	converter.		should reset the component.

Rounding Off Floats



Averaging Successive Readings



IRamp 🕅	tamp I2F AvgN M nc::IRamp In 10 In 10 Out 8.80 out 10.00 In 10.00 In 10.00		Avg10 averages over 10 samples but the data must change to	
func::IRamp Out 10			trigger a new sample. The AvgN component can be configured for	
		Reset false	the the number of samples but it samples every scan and not on just	
		TimeAvg math::TimeAvg Out 4.77	a change in value. The TimeAvg averages over a fixed period of	
	C	In 10.00 Time 100000 ms	time which is configurable. The output does not change until all	
			samples are obtained.	

On-Delays and Off-Delays





Using the Timer

		The Timer component will count
ConstBo	Timer 💟	down from a predetermined
types::ConstBool	Out true	amount when the Run input is true.
Out true	Run run Time 60 s Left 49 s	A constant integer component was used to set the time although the
		Timer component can be internally configured. The output will remain
ConstIn O		true during timing and transition
		input goes false. To begin a new
		timing period, the Run input must
		be cycled.

Using One-Shots — Mono-Stable Multivibrators



TickToc func::TickTock		B2P logic::B2P Out fal		
Out true		In	false	
			++	

The Boolean-to-Pulse (B2P) converter is actually a very simple single-shot in that it outputs a true for only one scan time when its input goes from false to true. There are no time settings. It is used when a pulse is required after detection of an event instead of a logic level.

Creating Ramps — A-Stable Multivibrators



Comparing Two Floats

Ramp 🔊	Cmpr func::Cmpr Xgy true Xey false	The comparator component (Cmpr) compares the the X input to that of the Y input. If X is less than Y, then
Out 76.34	Xiy false X 72.34	the Xly output is true. If X equals Y
ConstEl	Y 50.00	then Xey is true. If X is greater than Y then Xgy is true. Both inputs are floats
types::ConstFloat		and the outputs are Booleans. In this example the output of the Ramp is
		compared to that of a constant. Using the default values of the Ramp, the
		input X varies as a triangle between 0 and 100 every 10 seconds. You can
		watch how the comparator outputs change over this range.

A Simple Clock — the TickToc

TickToc func::TickTock Out false	Freq func::Freq Pps 1.000 /s Ppm 60.000 /min In false	The TickToc component provides a convenient clock from 1 to 10 pulses per second. However, because of the controller scan time and other processing overhead it is recommended to use its default value of one second. More accurate timing is available from a real- time clock.
		The Freq component can provide output values in pulses-per-second (Pps) or pulses-per-minute (Ppm). Because of the low-speed nature of these two components, the Ppm calculation will probably be the most useful.

Introducing Counters



Operating on Real-World Signals — Hysteresis and Limiting

Ramp func::Ramp	Hystere func::Hysteresis	The hysteresis component (Hystere) has
Out 65.15 ConstFl Sypes::ConstFloat Out 40.00	In 64.91 Out true Rising Edge 60.00 Falling Edge 40.00	separate rising-edge and falling-edge trip points when setting a trigger on a float variable. It is ideal for creating a digital event from a real-world analog input. Its output is Boolean.
ConstF1 types::ConstFloat 00.00	func::Limiter Cout 60.00 In 64.91 60.00 Low Lmt 40.00 High Lmt 60.00	The Limiter component restricts the range of a float variable by outputting a float that does not exceed the configurable low-limit (Low Lmt) or high- limit (High Lmt). The Limiter only limits the range of its output and does not scale the input float.

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Handling Non-Linear Signals

IRamp 🔤	I2F types::I2F	Lineari Ky
Put 9	In	Out 91.00
	Out 9.0	In 9.00
The linearize o	omponent (Linea	ri) operates on a float input and creates a piece-wise
The linearize of linear represe	component (Linea ntation of a non-l	ri) operates on a float input and creates a piece-wise inear input (such as a thermistor) or it can create a non
The linearize of linear represe linear piece-w	component (Linea ntation of a non-l rise representatio	ri) operates on a float input and creates a piece-wise inear input (such as a thermistor) or it can create a non n of a linear input. There is complete flexibility in
The linearize of linear represe linear piece-w defining the te	component (Linea ntation of a non-l rise representatio en X,Y coordinate	ri) operates on a float input and creates a piece-wise inear input (such as a thermistor) or it can create a non n of a linear input. There is complete flexibility in s along the output curve. The component determines

🗆 🔘 Meta	Group [1] »	
🗆 🔘 Out	56.50	
🗆 🔘 In	7.50	
🗆 🔘 🗙	0.00	In this example we will do the reverse of what is
🗆 🔘 Y0	0.00	create a non-linear output that approximates the
🗆 🔘 X1	1.00	equation Y=X*X over the range of X values from 0
🗆 🔘 Y1	1.00	to 9. We need to input corresponding values of Y
□ () X2	2.00	that obey the desired equation. To make it easy
🗆 🔘 Y2	4.00	restriction. For example, the square of 4 is 16 and
🗆 🔘 X3	3.00	the square of 5 is 25. We enter the X values as an
🗆 🔘 Y3	9.00	independent variable and then the Y value as the
🗆 🔘 X4	4.00	dependent variable. We need to be careful that the input does not exceed 9 in this example
🗆 🔘 Y4	16.00	because we do not define a corresponding value
🗆 🔘 X5	5.00	for Y above 9.
🗆 🔘 Y5	25.00	You can test the internelation by entering a value
🗆 🔘 X6	6.00	for X in the In slot assuming no link is connected
🗆 🔘 Y6	36.00	to the linearize component. This is done here.
🗆 🔘 X7	7.00	Notice that the result is 56.50 for an input value
🗆 🔘 Y7	49.00	of 7.5. The correct value would have been 56.25 which is very close
🗆 🔘 X8	8.00	which is very close.
🗆 🔘 Y8	64.00	
🗆 🔘 X9	9.00	
🗆 🔘 Y9	81.00	

Simple Set-Reset Flip Flop — Bi-Stable Multivibrator

ConstBo types::ConstBool SRLatch Out true func::SRLatch	The SRLatch appears to be straight- forward logic block. The output would
	become true if the set (S) pin is high and would go low if the reset (R) pin
ConstB1 types::ConstBool Out false	goes high. However, both the S and R pins are positive leading-edge
	sensitive. Regardless of their steady- state condition, the output (Out) will
transition from false-to-true during the	only change on the false-to-true transition of either input. If this occurs
because its state is tested last in the	on the S pin the output goes high and will remain high until the R pin does its
false.	transition.

The Loop Component — Basic PID Controller

ConstFl types::ConstFloat	۲	LP 🍙	The LP or loop component is one of the
Out	72.00	Sp 72.0	most complex components. It can provide
SpaceTp		Cv 72.50 Out 0.5	three modes of control P-proportional, I- integral, and D-derivative. In this
types::WriteFloat	72.50		example we will assume a temperature
Out	72.50	,	loop with a setpoint (Sp) of 72 degrees
			and a controlled variable (Cv) currently at 72.5 degrees which is the space
			temperature which we want to control.

LP (func::LP)		Enable must be configured true otherwise there
🗆 🔘 Meta	Group [1] »	is no control.
🗆 🔘 Enable	🔘 true 🔻	Kn is the propertional gain which defaults to 1
🗆 🔘 Sp	72.00	Notice that the error signal is Cy-Sp or 0.5. The
CV 🔘 CV	72.500	error multiplied by the proportional gain of 1
🗆 🔘 Out	0.50	yields an output of 0.50. If the Ki and Kd factors
🗆 🔘 Кр	1.000000 [0.000000 - +inf]	are used, their contributions are also multiplied
DOK	0.000000 /min [0.000000 - +inf]	gain in units of resets per minute. It is
🗆 🔘 Kd	0.000000 s [0.000000 - +inf]	multiplied by the error signal. Kd is the
🗆 🔘 Max	100.000000	derivative gain in seconds and it is also
🗆 🔘 Min	0.000000	multiplied by the error signal.
🗆 🔘 Bias	0.000000	Min and Max are the limits of the output signal.
🗆 🔘 Max Delta	0.000000 [0.000000 - +inf]	They can be set to any value. Bias can offset the
Direct	◯ true 💌	output regardless of the error. Max Delta sets
🗆 🔘 Ex Time	1000 ms [0 - max]	the rate of change of the output within the output limits. This will slow the output swing.
Bias only ap control. Wl reset-windu limiting the	plies to proportional-only (P) hen using a PI controller, up can be minimized by output range.	For a cooling application set Direct to true. For heating set it for false. The loop equation is solved each execute time

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Linear Sequencer — Bar-Graph Representation of a Float

LSeq hvac::LSeq 2 The linear sequencer (LSeq) 78.00 In provides a digital representation of Delta 10.00 an input float similar in operation D On true Out1 to a bar graph on audio equipment. Out2 true It is easier to understand its IRamp func::IRamp 12F Out3 true ~ Э types::I2F Out4 true operation using an integer input. In Out Out 78 78.00 Out5 true There are 16 possible Boolean Out6 Out7 true true false false false false false false outputs plus one overflow (Ovfl) Out8 flag. The input ramp provides a Out9 Out10 triangle wave from 0 to 100. The Out11 sequencer was configured for a 0 Out12 Out13 minimum input and a 100 maximum Out14 input. The maximum number of false false Out15 Out16 Ovfl outputs was configured for 9 yielding a Delta of 10.

🗆 🔘 Meta	Group [1] »		
🗆 🔘 In	60.00		
🗆 🔘 In Min	0.00		
🗆 🔘 In Max	100.00		
🗆 🔘 Num Outs	9	[1 - 16]	The range of the linear sequencer is
🗆 🔘 Delta	10.00		configured using In Min at the low-end
🗆 🔘 D On	6	[0 - 255]	number of outputs (Num Outs)
🗆 🔘 Out1	🔘 true		determines the difference (Delta)
🗆 🔘 Out2	🔘 true		between successive outputs turning on.
🗆 🔘 Out3	🔘 true		In this case the range is 100 and the number of desired outputs is 9 Divide 100
🗆 🔘 Out4	🔘 true		by Num Outs + 1 and you will get a Delta of
🗆 🔘 Out5	🔘 true		10.
Out6	🔘 true		You will notice that the input (In) is at 60
🗆 🔘 Out7	ight false		and D On is indicating that six outputs are
Out8	ight false		on. With an input between 0-9, there are
🗆 🔘 Out9	S false		no outputs on but once you hit a decade
0ut10	S false		outputs will come on. At the maximum of
🗆 🔘 Out11	false		100, 9 lights will be on. If the input
0ut12	S false		exceeds the maximum intended, the
🗆 🔘 Out13	S false		outputs will remain as specified by Num
0ut14	false		Outs.
🗆 🔘 Out15) false		
🗆 🔘 Out16) false		
🗆 🔘 Ovfl) false		

Reheat Sequencer — Four Staged Outputs from a Float Input

Ramp func::Ramp Out 2.43	ReheatS hvac::ReheatSeq Out1 true Out2 true Out3 false Out4 false In 2.40 D On 2	The reheat sequencer (ReheatS) provides a linear sequence of up to four outputs based upon the input float (In). The threshold for the four outputs can be configured for increasing values of the input. As the input increases to each threshold, the corresponding output will
		go on. As the input decreases below the threshold, the corresponding output will remain on until the Hysteresis value is
		exceeded.

ReheatS (hvac::	ReheatSeq)	
🗆 🔘 Meta	Group [1] »	
🗆 🔘 Out1	🔘 true	Enable must to true otherwise the outputs
🗆 🔘 Out2	🔘 true	to be false.
🗆 🔘 Out3	🔘 true	There are four possible threshold settings
🗆 🔘 Out4	false	corresponding to four outputs. As the
🗆 🔘 In	2.93	input signal increases to each threshold its corresponding output goes on and stays on
🗆 🔘 Enable	🔘 true 🔻	until the input drops below the threshold
🗆 🔘 D On	3 [0 - 255]	plus the value of the hysteresis.
🗆 🔘 Hysteresis	0.25	The input signal is decreasing but it has not
🗆 🔘 Threshold 1	1.00	exceeded the amount of the threshold so
Threshold2	2.00	output 3 (Out3) remains set. Once the
🗆 🔘 Threshold3	3.00	signal is below 2.75, output 3 will go off.
Threshold4	4.00	

Reset — Scaling a Float Input between Two Limits

Ramp func::Ramp	Reset hvac::Reset Out 83.61 In 28.67	The reset component (Reset) will scale the output linearly between two limits. The input range must be
		configured by setting In Min and In
		Max. The corresponding output for
		those two points must be
		configured as Out Min and Out Max.
		If the input signal exceeds the
		defined input range, the output will
		be clamped to one of the two
		output limits.

Reset (hvac::	Reset)	
🗆 🔘 Meta	Group [1] »	
🗆 🔘 Out	81.22	In this example we are converting degrees Celsius to
🗆 🔘 In	27.34	degrees Fahrenheit within the 0-100 degree Celsius
🗆 🔘 In Min	0.00	range. Therefore we set Out Min and Out Max to the
🗆 🔘 In Max	100.00	between these two limits will be interpolated thereby
🗆 🔘 Out Min	32.00	providing the correct Fahrenheit values.
🗆 🔘 Out Max	212.00	

Tstat — **Basic On/Off Temperature Controller**

types::Const	Float	Tstat	R	The Tstat is an on/off temperature
Out	72.00	hvac::Tstat	1.00	controller for either heating or cooling
		Is Heating Sp	1.00 true 72.00	For heating configure the Is Heating bit
SpaceTp types::Write In	Float 71.40	Out Raise	/1.40 true true	the Diff value. If the controlled variable
Out	/1.40	Lower	Taise	half the Diff value, the output (Out) will become true and stay set until Cv
				deviates from the setpoint by a like amount in the other direction. In this
				way Diff also provides hysteresis. The Raise and Lower outputs are a function
				of the Is Heating setting. If Is Heating is true, Out=Lower, otherwise Out= Raise.

Real-Time Clock and Scheduling

datetimeStd::DateTimeSe	erviceStd '88"
Nanos 42663416	400000000 ns
Hour	21
Minute	29
Second	24
Year	2013
Month	7
Day	8
Day Of Week	1
DailySc basicSchedule::DailySch	eduleBool 🔘
Out	false
DailyS1	0
basicSchedule::DailySch	eduleFloat
Out	0.00

The DateTim component provides real-time information. There is no need to place it on the wiresheet. However, if you need specific information from the component for driving logic, you can connect to the various integer outputs such as Hour, Minute and Second. There are two schedule components which have different output types. One is for Boolean and the other for float. There is no need to connect the DateTim component to either of the schedulers. Each scheduler can handle two events over the 24 hour period by configuring the time and duration of each event. The output of each schedule will change with each event. If more events or more outputs are needed, multiple schedulers can be placed on the wiresheet.

DailyS1 (basicSchedule::DailyScheduleFloat)				
🗆 🔘 Meta	Group [1] »	Configuration of the two scheduler		
🗆 🔘 Start 1	12:00 AM	version. Val1 and Val2 need to be		
🗆 🔘 Dur 1	00000h 00m 🚔 [0ms - 1day]	specified along with the start times		
🗆 🔘 Start2	12:00 AM	(Start1 and Start2) and the durations		
🗆 🔘 Dur2	00000h 00m 🚔 [0ms - 1day]	(Dur1 and Dur2). The output (Out)		
🗆 🔘 Val1	0.00	will assert either Val1 or Val2 during		
🗆 🔘 Val2	0.00	the scheduled times. If neither are		
🗆 🔘 Def Val	0.00	configured.		
🗆 🔘 Out	0.00	-		

Priority Arrays

		Priority array (Priorit) components exist
ConstBo types::ConstBool	Priorit pricomp::PrioritizedBool	for Boolean, float and integer variables.
Out null	In10 null	Up to 16 levels of priority from In1 to
	In16 true	In16 can be assigned. In1 has the
	Out true	highest priority and Int6 the lowest
ConstB1 types::ConstBool		With few exceptions, all can be pinned
Out true		out. If a priority level is not assigned it
		is marked as a Null and therefor
ConstFl (0)	Priori1 pricomp::PrioritizedFloat	ignored. If a Null is inputted to the
Out 5.00	In10 5.00	priority array as shown in the top-most
	In16 6.00	provide the activity and the second states in the
	Out 5.00	example, the priority array will ignore it
ConstF1 types::ConstFloat		and choose the next in line input. The
Out 6.00		Boolean version of the priority array has
		two timer settings - one for minimum
Constra	(Pariani2	active time and minimum inactive time.
types::ConstInt	pricomp::PrioritizedInt	If the highest priority device changes
Out 25	In5 1	In the highest phonty device changes
4	In10 25	from false to true and then back to
	In16 35	false the priority component will
ConstI1	Out 1	laise, the phoney component with
types::ConstInt		maintain the event for the configured
Out 35		times.
Constant Con	There is a Fallback se	tting in each array that can be specified.
types::ConstInt	If no valid priority inp	out exists, the Fallback value is
Out 1	transferred to the ou	tput.
	The Override Exp Tim	e guards against the possibility of an
	indefinite override o	ondition.

<u> </u>			
Views	•		
Actions	•	Emergency Set Active	
Cut	Ctrl+X	Emergency Set Inactive	
Copy	Ctrl+C	Emergency Auto	
Paste	Ctrl+V	Manual Set Active	
Paste Special		Manual Set Inactive	
Duplicate	Ctrl+D	Manual Auto	
Delete	Delete		
Link Mark Link From Link To		When you right-click on the priority component and select actions you will have several choices for overriding the current priority selection made by the component. The override choices vary	
Rename	Ctrl+R	depending upon the type of variable supported by the priority component. In this example, the Priority Boolean was selected. Setting an override using a tool is only temporary. Eventually, the	
- C Pin Slots		component will time out and revert to normal priority selection.	

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